

Form PTO-1449

LIST OF PRIOR ART CITED
BY APPLICANT

(Use several sheets if necessary)

ATTORNEY DOCKET NO.

01-379

SERIAL NO.

Not Assigned

APPLICANT **Weber**FILING DATE **November 5, 2001**GROUP ART UNIT **Not Assigned**1c978 U.S. PTO
09/993015

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO

OTHER PRIOR ART (including author, title, date, pertinent page, etc.)

VK	AA	Kagaris, D. "The VLSI Handbook", Chapter 67 entitled ATPG and BIST, pp 67-1 through 67-14			

DATE CONSIDERED

EXAMINER

05/15/03

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.